

**IN THE SPECIFICATION:**

Please replace the paragraph on page 6, lines 17-23, with the following rewritten paragraph:

For example, host devices 126 may write data to system memory 140 in data area 142 for subsequent storage in one of storage devices 122, 130. As another example, data may be written from one of storage devices 122, 130 to system memory and read from system memory to be transferred to one of host devices 126. Direct memory access (DMA) controller 148 also transfers blocks of data from memory to storage devices. Furthermore, array parity control 150 takes data in blocks of memory, performs calculations, and creates new data. The array parity control is used in striping data across storage devices.